

	L #	Hits	Search Text	DBs
1	L1	5648	(361/760-768,770,771,782,783,784,803,804,807,811,303-305,306.1).ccls.	USP AT; US-P GPU B
2	L2	3960	(174/250,253,255,259,260,261).ccls.	USP AT; US-P GPU B
3	L3	4380	(333/181,184,185,202,204,236,238,245,246,247).ccls.	USP AT; US-P GPU B
4	L4	3698	(257/296,307,664,678,728,904,924).ccls.	USP AT; US-P GPU B
5	L5	2318	(439/68,69,83,620).ccls.	USP AT; US-P GPU B
6	L6	1779 5	1 2 3 4 5	USP AT; US-P GPU B
7	L7	1736	6 and @pd>=20021006	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
8	L8	124	7 and (capacitive\$2 with (signal\$4 coupl\$3))	USP AT; US-P GPU B
9	L9	168	7 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))	USP AT; US-P GPU B
10	L10	130	9 not 8	USP AT; US-P GPU B
11	L11	23	7 and (non\$1contact contactless)	USP AT; US-P GPU B
12	L12	15	11 not (8 10)	USP AT; US-P GPU B
13	L13	726	7 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
14	L14	595	13 not (8 10 12)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
15	L15	235	14 and coupl\$4	USP AT; US-P GPU B
16	L16	269	361/794.ccls. <i>NOT Searched.</i>	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
17	L17	103	("3585368" "3593319" "3604900" "3621478" "3629733" "3719804" "3869082" "3974332" "4139827" "4144485" "4145624" "4280119" "4480178" "4553026" "4633291" "4634847" "4700152" "4752680" "4763340" "4795898" "4798322" "4816653" "4816654" "4818855" "4835373" "4841128" "4853523" "4876535" "4918416" "4931991" "4953123" "5012321" "5021767" "5027191" "5073761" "5148263" "5159181" "5175418" "5212402" "5229652" "5266821" "5309324" "5324205" "5378887" "5435733" "5451763" "5471040" "5532658" "5572441" "5579207" "5583378" "5589709" "5593322" "5594233" "5598029" "5598032" "5629838" "5640306" "5652423" "5668399" "5672911" "5682061" "5701032" "5701037"	USP AT
18	L18	1807	(257/723,724).ccls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
19	L19	44	18 and (capacitive\$2 with (signal\$4 coupl\$3))	USP AT; US-P GPU B
20	L20	30	18 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))	USP AT; US-P GPU B
21	L21	29	18 and (non\$1contact contactless)	USP AT; US-P GPU B
22	L22	99	19 20 21	USP AT; US-P GPU B
23	L23	536	18 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
24	L24	261	23 and coupl\$4	USP AT; US-P GPU B
25	L25	221	24 not (8 10 12 22)	USP AT; US-P GPU B